

EAST - [C10058187.wsp:1]

File View Edit Tools Window Help

Drafts

Pending

Active

- L1: (1195) (router routing) with (accumulat\$ arithmetic\$)
- L2: (23520) "708"/\$.ccls.
- L3: (11533) "712"/\$.ccls.
- L4: (22577) "710"/\$.ccls.
- L6: (138) 1 and (3 or 4)
- L7: (223) 5 or 6
- L5: (117) 1 and 2
- L8: (918) banyan
- L9: (8) 1 and 8
- L11: (3928) bitwise bitfield
- L12: (23) 1 and 11

Failed

Saved

Favorites

Tagged (3)

UDC

Queue

Trash

Searcher: [List] [Spelled] [Index] [Edit]

DBs: USPCPUB, USPAT, EPO, JPO, DER [Purch]

Default operator: OR [v] [Highlight all hit terms only]

BRIS form, IS&R form, image, Text, HTML

	U	Document ID	Issue D	P	Title	Current OR	Current XRef	R	Inventor
1	<input checked="" type="checkbox"/>	US 4868776 A	1989091	1	Fast fourier transform architecture using	708/405	708/404; 708/408		Gray; Joseph et al.
2	<input checked="" type="checkbox"/>	US 6781408 B1	2004082	1	Programmable logic device with routing	326/41	326/38		Langhammer; Martin
3	<input checked="" type="checkbox"/>	US 6434583 B1	2002081	1	Fast fourier transform	708/409	708/622		Dapper; Mark

Ready

NUM

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